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Paragraph beginning at page 5, line 11 has been amended as follows:

-- A number of embodiments have been described.

Nevertheless, one of ordinary skill will understand that variations are possible. For example, while the invention has been described in terms of signal routing to a RAMBUS™ device, this scheme is useful in routing signals to other types of memory devices and even to other system components.

Accordingly, other embodiments are within the scope of the following claims. --

## In the claims:

Claims 5, 7 and 17 have been amended as follows:

- -- 5. (Twice Amended) The system of claim 4, wherein the first signal line and the portion of the second signal line that is routed substantially parallel to the first signal line are separated by a distance about equal to said widths.
- 7. (Amended) The system of claim 1, wherein the memory unit comprises a RAMBUS  $^{TM}$  device.
- 17. (Twice Amended) The method of claim 16, further comprising forming the first signal line and the first portion of the second signal line that is routed substantially parallel to the first signal line to have substantially equal widths. --